

Electronic Acknowledgement Receipt

EFS ID:	7570764
Application Number:	10595303
International Application Number:	
Confirmation Number:	7114
Title of Invention:	Method and Device For Secure, Insulated and Electrically Conductive Assembling Of Treated Semiconductor Wafers
First Named Inventor/Applicant Name:	Roy Knechtel
Customer Number:	21967
Filer:	Yisun Song/Robin Taylor
Filer Authorized By:	Yisun Song
Attorney Docket Number:	60291.000048
Receipt Date:	07-MAY-2010
Filing Date:	19-MAY-2006
Time Stamp:	13:40:49
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$ 1300
RAM confirmation Number	8395
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	RCE.pdf	45294	no	1
			bc48961045f300e676c4af832ddd4c9345d0e17c		

Warnings:

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Information:

2		Amendment.PDF	519859	yes	15
			b0e0d35194d6251acd0fa1c32ba907ed5f99bbc1		

Multipart Description/PDF files in .zip description

	Document Description	Start	End
	Amendment Submitted/Entered with Filing of CPA/RCE	1	1
	Claims	2	6
	Applicant Arguments/Remarks Made in an Amendment	7	15

Warnings:

Information:

3	Fee Worksheet (PTO-875)	EXT.pdf	33489	no	1
			ef968b91b37bf7a0fb349dd0a631f87f8ecd37bd		

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Information:

4		IDS.pdf	235376	yes	6
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	Document Description	Start	End
	Information Disclosure Statement (IDS) Filed (SB/08)	1	3
	NPL Documents	4	6

Warnings:

Information:

5	Fee Worksheet (PTO-875)	fee-info.pdf	32253	no	2
			4f09b406b1356b088d85a0f3b55abba93b48bd73		

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Total Files Size (in bytes):	866271
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